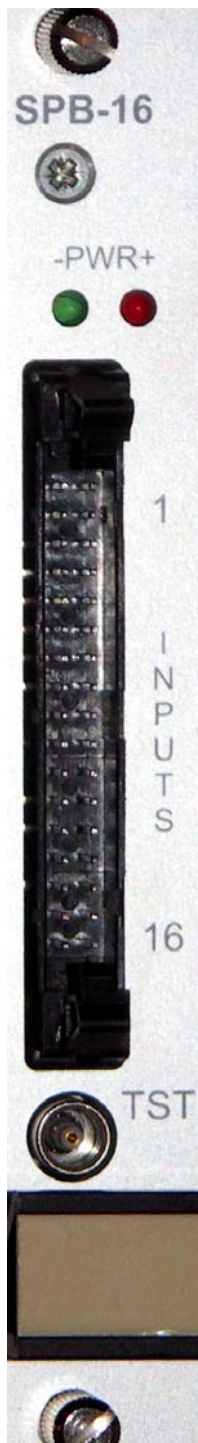


SPB-16 (Wire Discriminator Board)

SPB-16 is a 16-channel discriminator with an adjustable threshold. The outputs are LVDS signals. The module is in fact a redesign of ([link](#)). The main features of the redesign are:

- New form factor - Eurocard 1 unit wide 3U height
- PCB size - 160 mm x 100 mm
- Remote power off possibility
- Temperature sensor added

Fig. 1 SPB-16 front panel view.



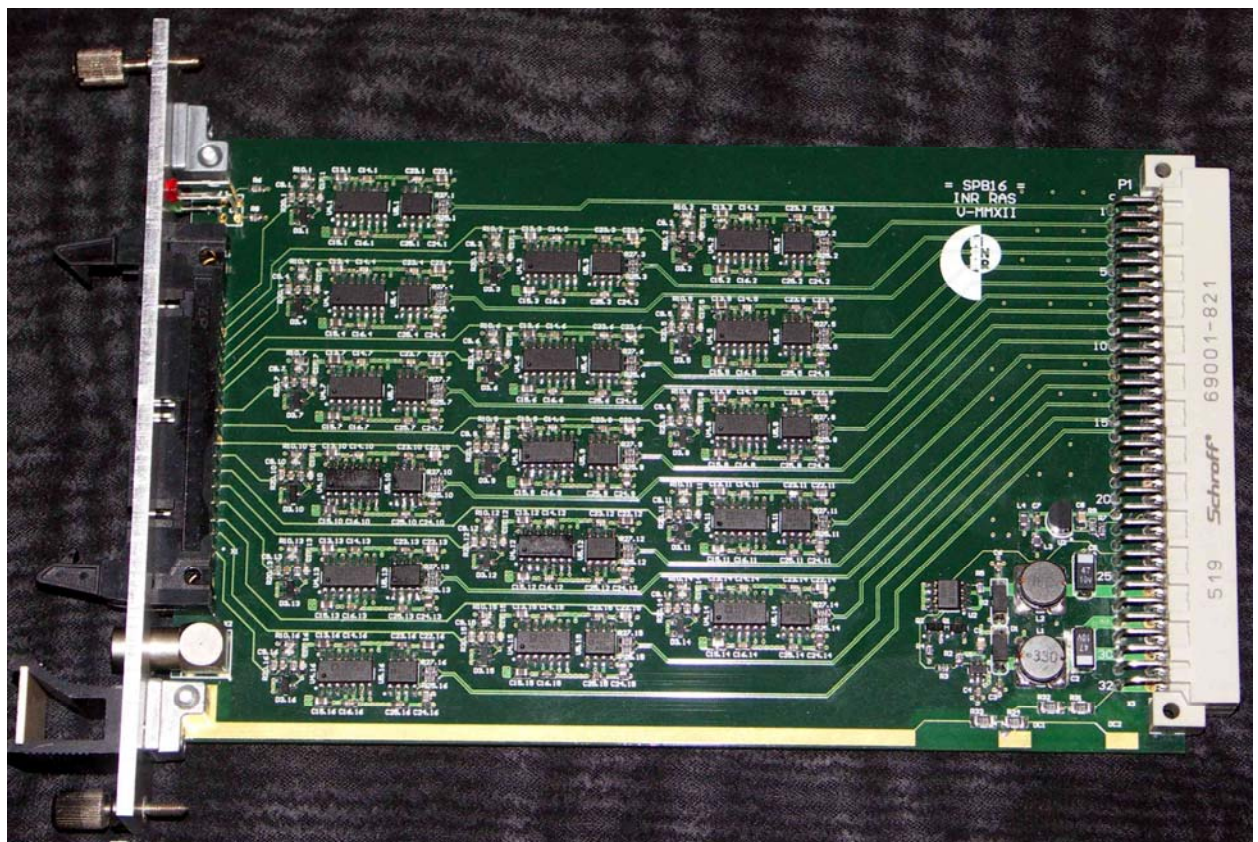
On the front panel side there are:

- The input connector (IDC 34 pins)
- LEMO connector for test signal (input)
- Power LEDs (+5V – red, -5V - green)

On the back side:

- The output connector (DIN 41612 type C connector, 64ac male 3 rows x 32 pins, the middle row is empty)

Fig. 2 Picture of the PCB



The main components of the circuit are:

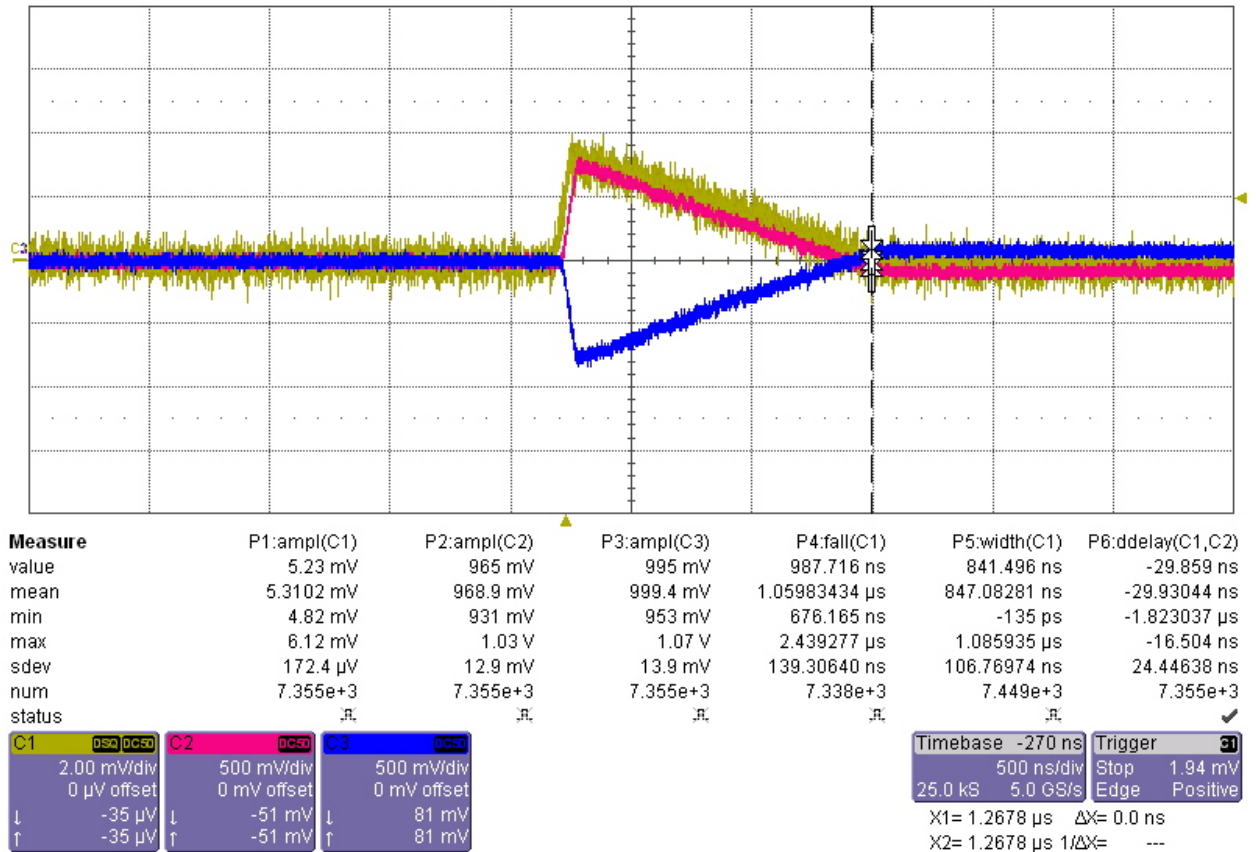
- A single channel triple amplifier (AD8013)
- A differential amplifier (AD8132)
- a temperature sensor (LM35DZ)
- power switches (MIC2005-0.5YM6 and VNS3NV04D)

The module requires 2 power supply voltages: +5V and -5V. The derived currents are typically 390 mA (+5V) and 390 mA (-5V) without load .

Test of the SPB

The fig. 3 shows the response of the module on positive pulses 5,3 mV amplitude with leading edge 50 ns and trailing edge 1000 ns.

Fig. 3 Response of the SPB-16



The table 1 shows the SPB-16 characteristics.

Table 1. SPB-16 characteristics

| | |
|--|------------------------------------|
| Number of channels | 16 |
| Limiting counting rate of each channel | $1 * 10^5 / s$ |
| Amplification factor | 850 |
| Dynamic range | 100 FC – 1000FC |
| Supply current, no load | Upos 5V, 390 mA; Uneg -5V, -390 mA |
| Output amplitude | 1,5V max |

Temperature output is $10 \text{ mV} / C^0$.

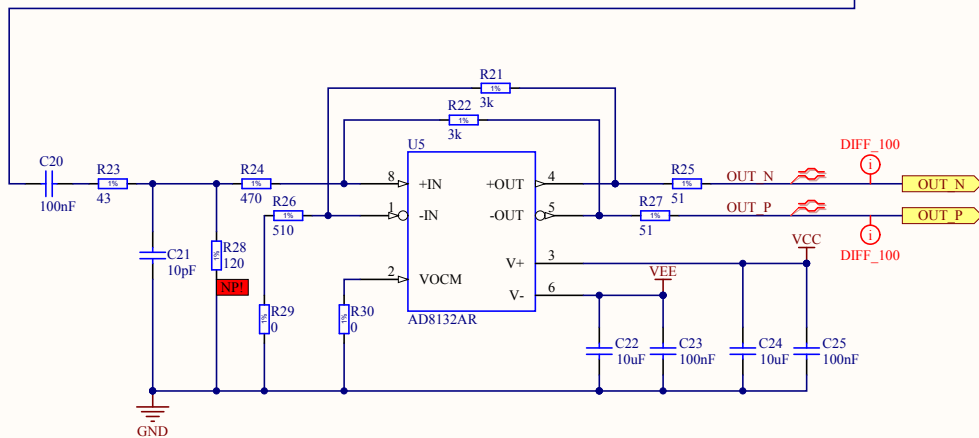
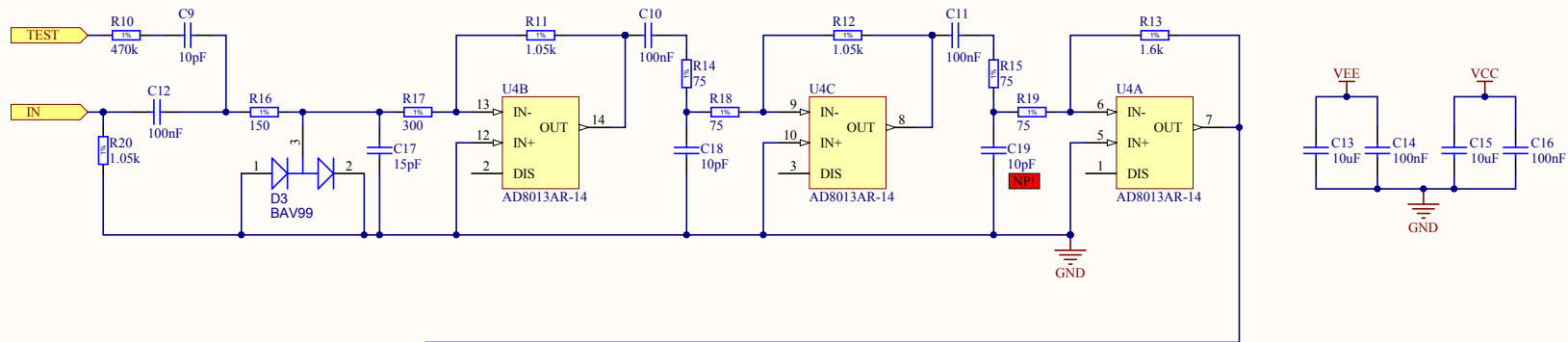
To turn the module's power off, connect the "OFF" contact to GND or supply to it 5V TTL/CMOS logic "0".

Table 2. Input connector IDC-34 pinout, front view

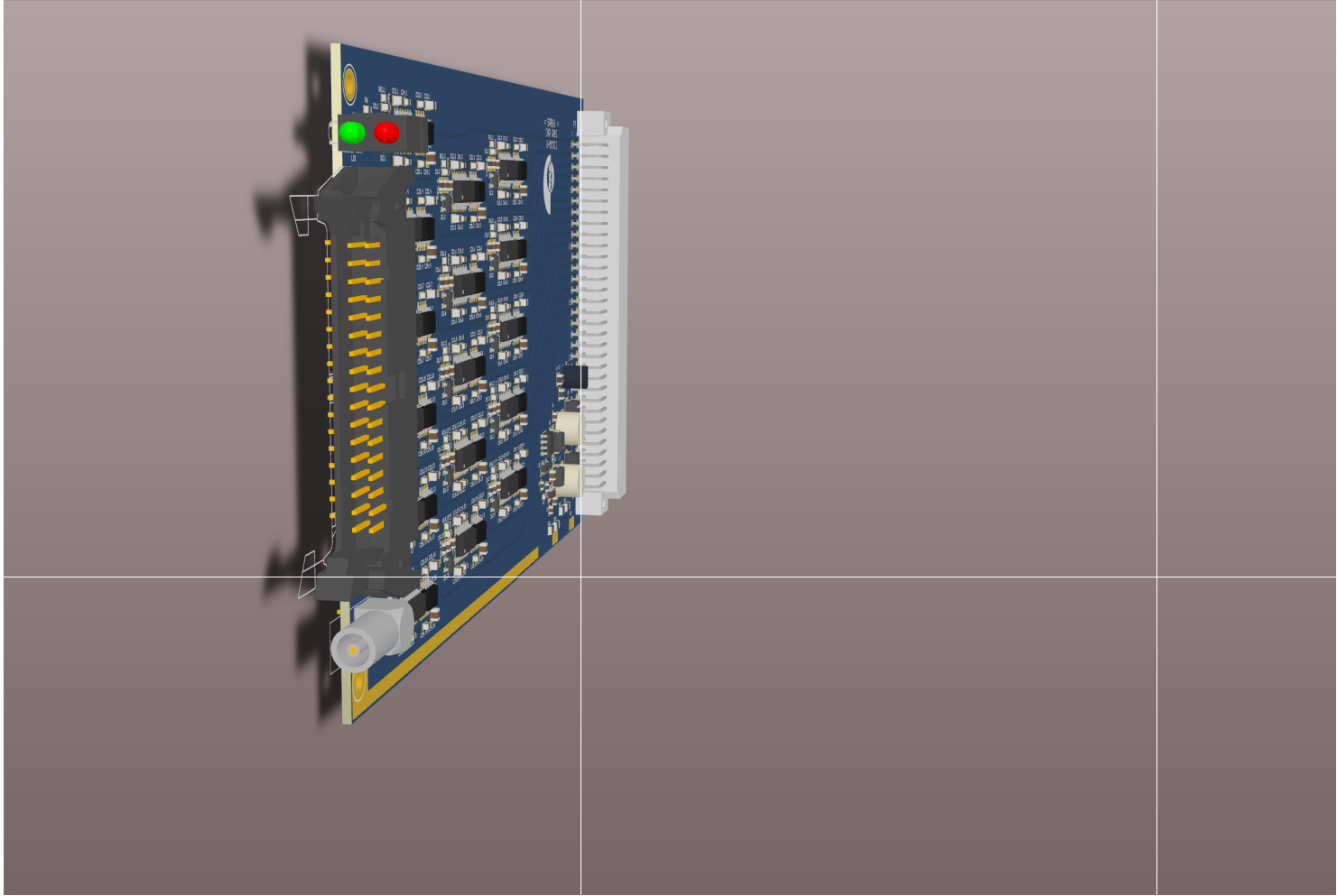
| Pin # | | Pin # | |
|-------|-------|-------|-------|
| 34 | FRAME | 33 | FRAME |
| 32 | GND | 31 | IN1 |
| 30 | GND | 29 | IN2 |
| 28 | GND | 27 | IN3 |
| 26 | GND | 25 | IN4 |
| 24 | GND | 23 | IN5 |
| 22 | GND | 21 | IN6 |
| 20 | GND | 19 | IN7 |
| 18 | GND | 17 | IN8 |
| 16 | GND | 15 | IN9 |
| 14 | GND | 13 | IN10 |
| 12 | GND | 11 | IN11 |
| 10 | GND | 9 | IN12 |
| 8 | GND | 7 | IN13 |
| 6 | GND | 5 | IN14 |
| 4 | GND | 3 | IN15 |
| 2 | GND | 1 | IN16 |

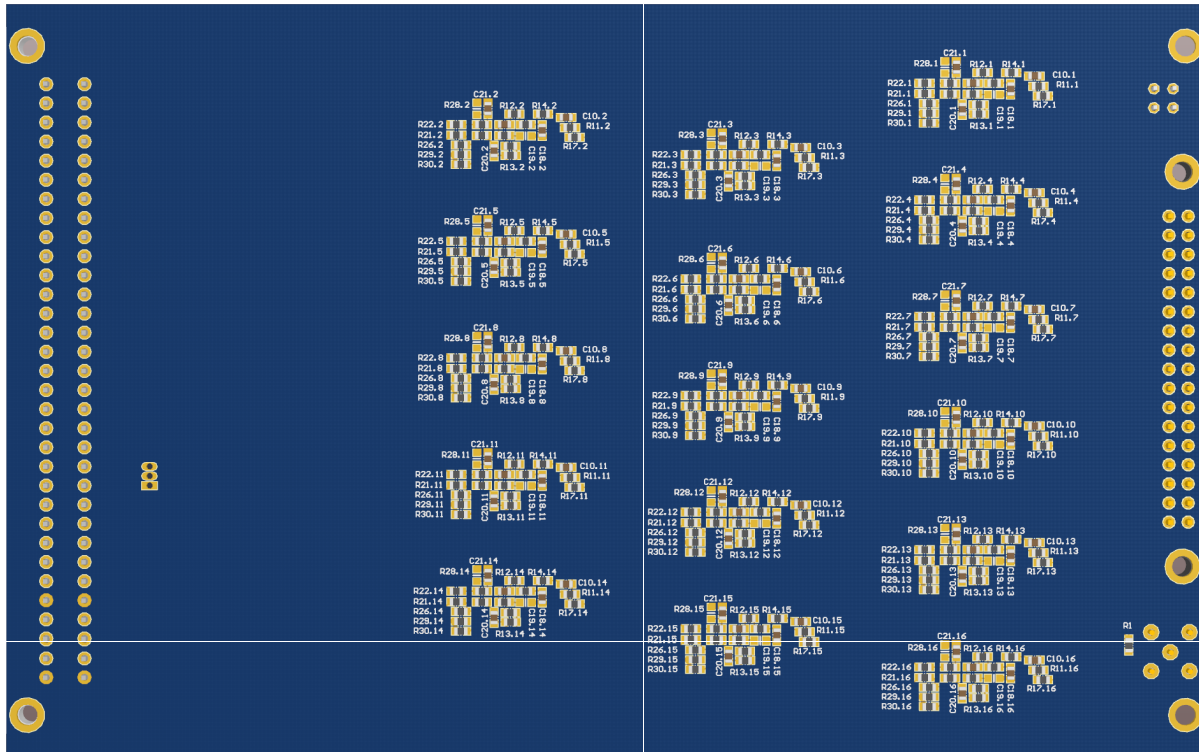
Table 3. Output connector pinout, rear view

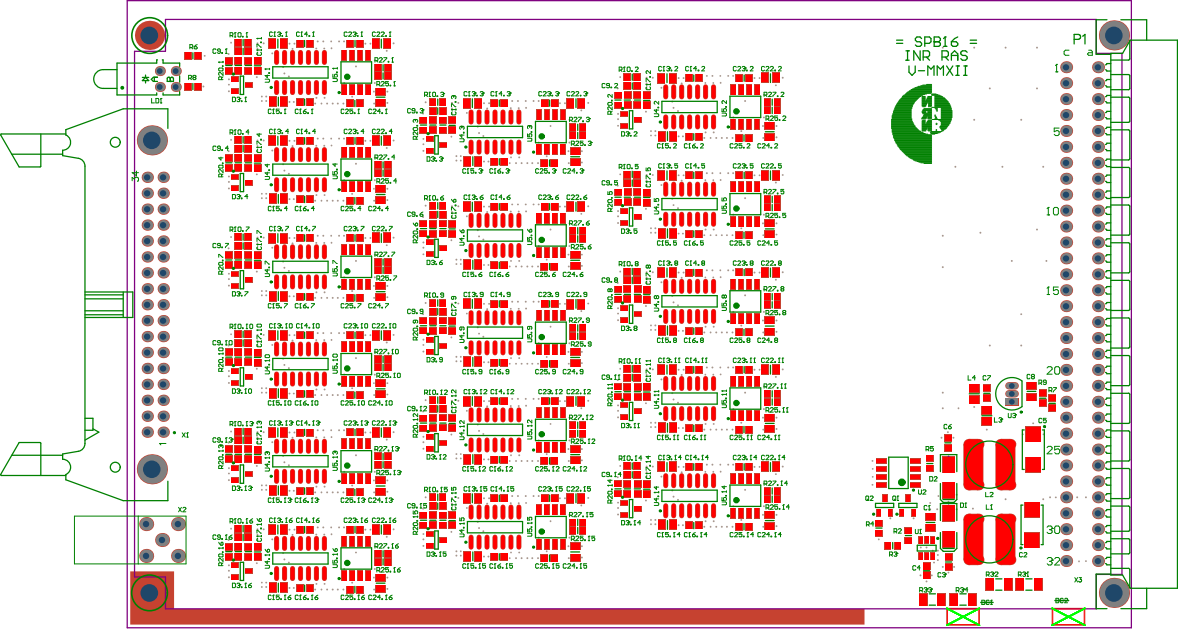
| Pin # | c | b | a |
|-------|--------|---|--------|
| 1 | OUT1- | | OUT1+ |
| 2 | OUT2- | | OUT2+ |
| 3 | OUT3- | | OUT3+ |
| 4 | OUT4- | | OUT4+ |
| 5 | OUT5- | | OUT5+ |
| 6 | OUT6- | | OUT6+ |
| 7 | OUT7- | | OUT7+ |
| 8 | OUT8- | | OUT8+ |
| 9 | OUT9- | | OUT9+ |
| 10 | OUT10- | | OUT10+ |
| 11 | OUT11- | | OUT11+ |
| 12 | OUT12- | | OUT12+ |
| 13 | OUT13- | | OUT13+ |
| 14 | OUT14- | | OUT14+ |
| 15 | OUT15- | | OUT15+ |
| 16 | OUT16- | | OUT16+ |
| 17 | | | |
| 18 | FRAME | | FRAME |
| 19 | FRAME | | FRAME |
| 20 | | | |
| 21 | T_GND | | TEMP |
| 22 | | | |
| 23 | | | |
| 24 | | | OFF |
| 25 | | | |
| 26 | 5V- | | 5V- |
| 27 | | | |
| 28 | GND | | GND |
| 29 | | | |
| 30 | 5V+ | | 5V+ |
| 31 | | | |
| 32 | GND | | GND |

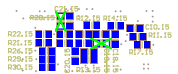
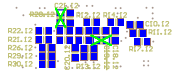
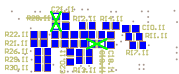
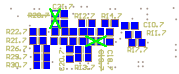
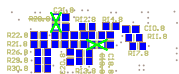
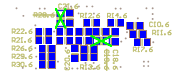
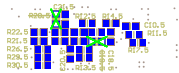
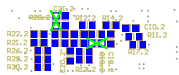


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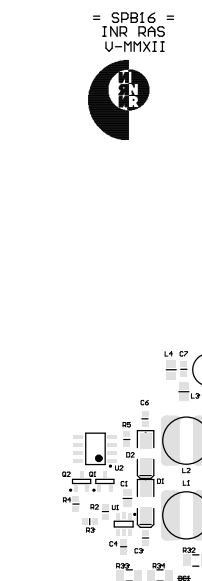
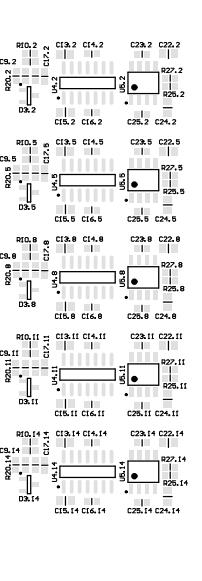
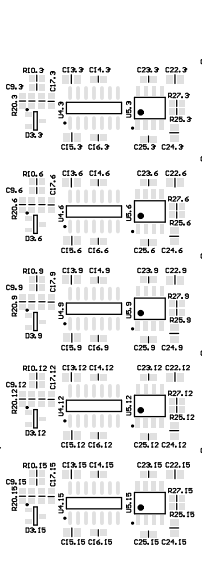
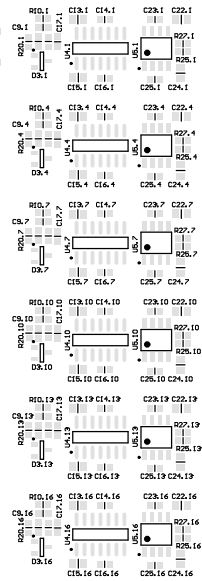
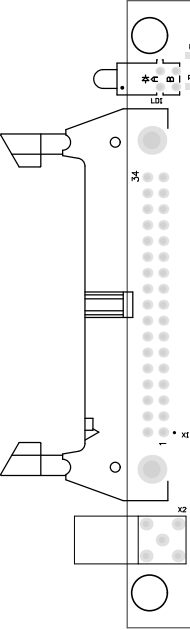




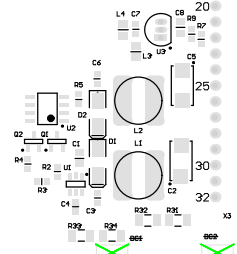
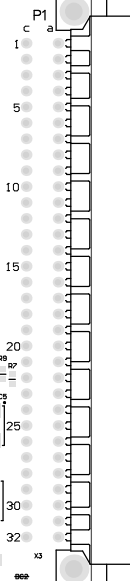




RI
■



= SPB16 =
INR RAS
V-MMXII



100k
3k 470100nF 75 100nF
3k 43 75 1.05k 1.05k
510 100k 1.6k 300
0 0

100k
3k 470100nF 75 100nF
3k 43 75 1.05k 1.05k
510 100k 1.6k 300
0 0

100k
3k 470100nF 75 100nF
3k 43 75 1.05k 1.05k
510 100k 1.6k 300
0 0

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3k 470100nF 75 100nF
3k 43 75 1.05k 1.05k
510 100k 1.6k 300
0 0

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3k 470100nF 75 100nF
3k 43 75 1.05k 1.05k
510 100k 1.6k 300
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3k 470100nF 75 100nF
3k 43 75 1.05k 1.05k
510 100k 1.6k 300
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510 100k 1.6k 300
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510 100k 1.6k 300
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510 100k 1.6k 300
0 0

100k
3k 470100nF 75 100nF
3k 43 75 1.05k 1.05k
510 100k 1.6k 300
0 0

100k
3k 470100nF 75 100nF
3k 43 75 1.05k 1.05k
510 100k 1.6k 300
0 0

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510 100k 1.6k 300
0 0

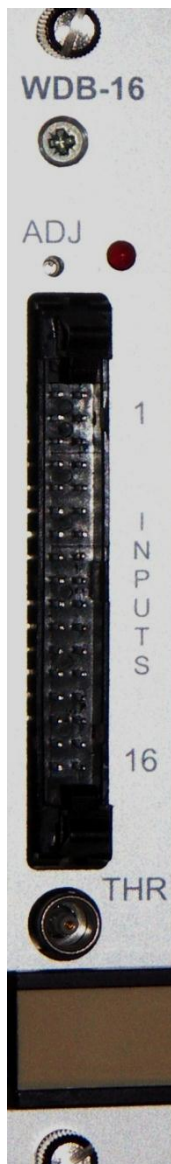
100k
3k 470100nF 75 100nF
3k 43 75 1.05k 1.05k
510 100k 1.6k 300
0 0

WDB-16 (Wire Discriminator Board)

WDB-16 is a 16-channel discriminator with an adjustable threshold. The outputs are LVDS signals. The module is in fact a redesign of [\(link\)](#). The main features of the redesign are:

- New form factor - Eurocard 1 unit wide 3U height
- PCB size - 160 mm x 100 mm
- Remote power off possibility
- Temperature sensor added

Fig. 1 WDB-16 front panel view.



On the front panel side there are:

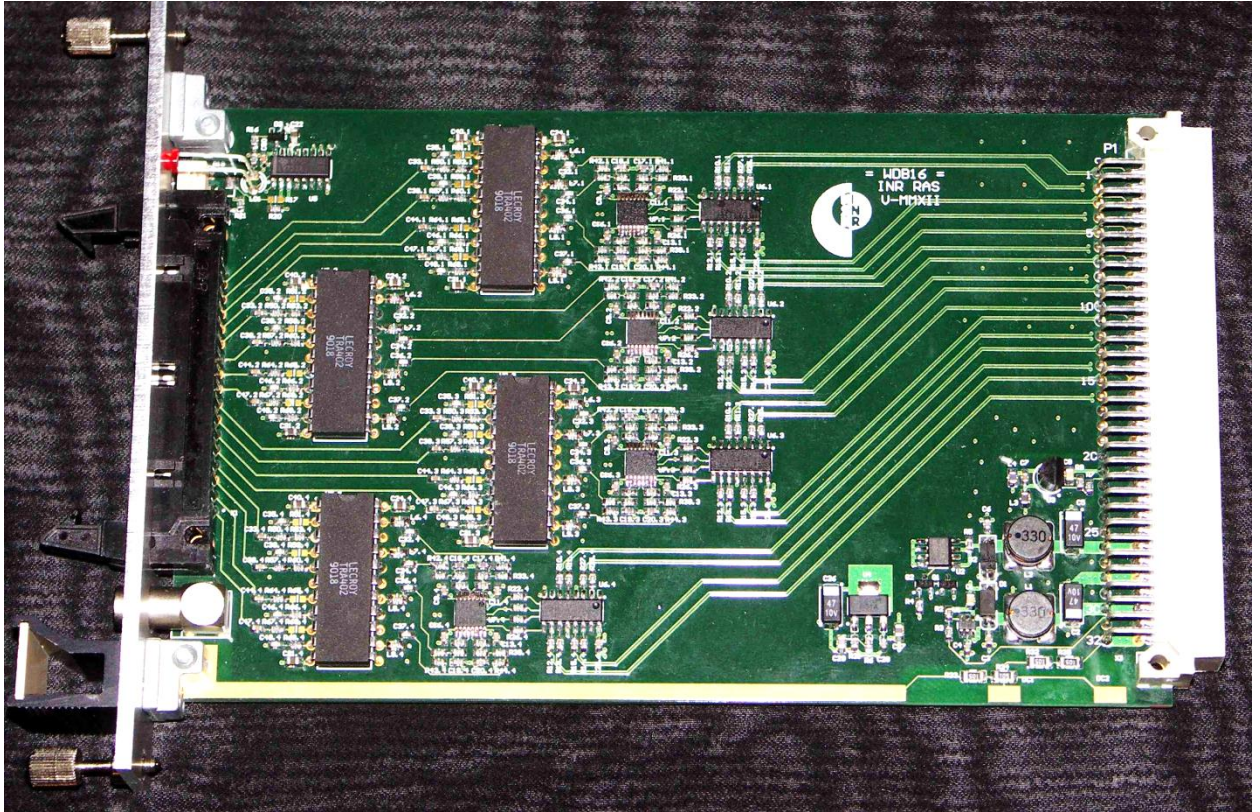
- The input connector (IDC 34 pins)
- LEMO connector for threshold measurement
- The trimmer screw for threshold regulation

- A red led (trigger counts of channel no. 2)

On the back side:

- The output connector (DIN 41612 type C connector, 64ac male 3 rows x 32 pins, the middle row is empty)

Fig. 2 Picture of the PCB



The main components of the circuit are:

- a monolithic 4 channel amplifier (Lecroy TRA402)
- a 4 channel voltage comparator (MAX 9201)
- a 4 channel LVDS driver (MAX 9124)
- a temperature sensor (LM35DZ)
- power switches (MIC2005-0.5YM6 and VNS3NV04D)

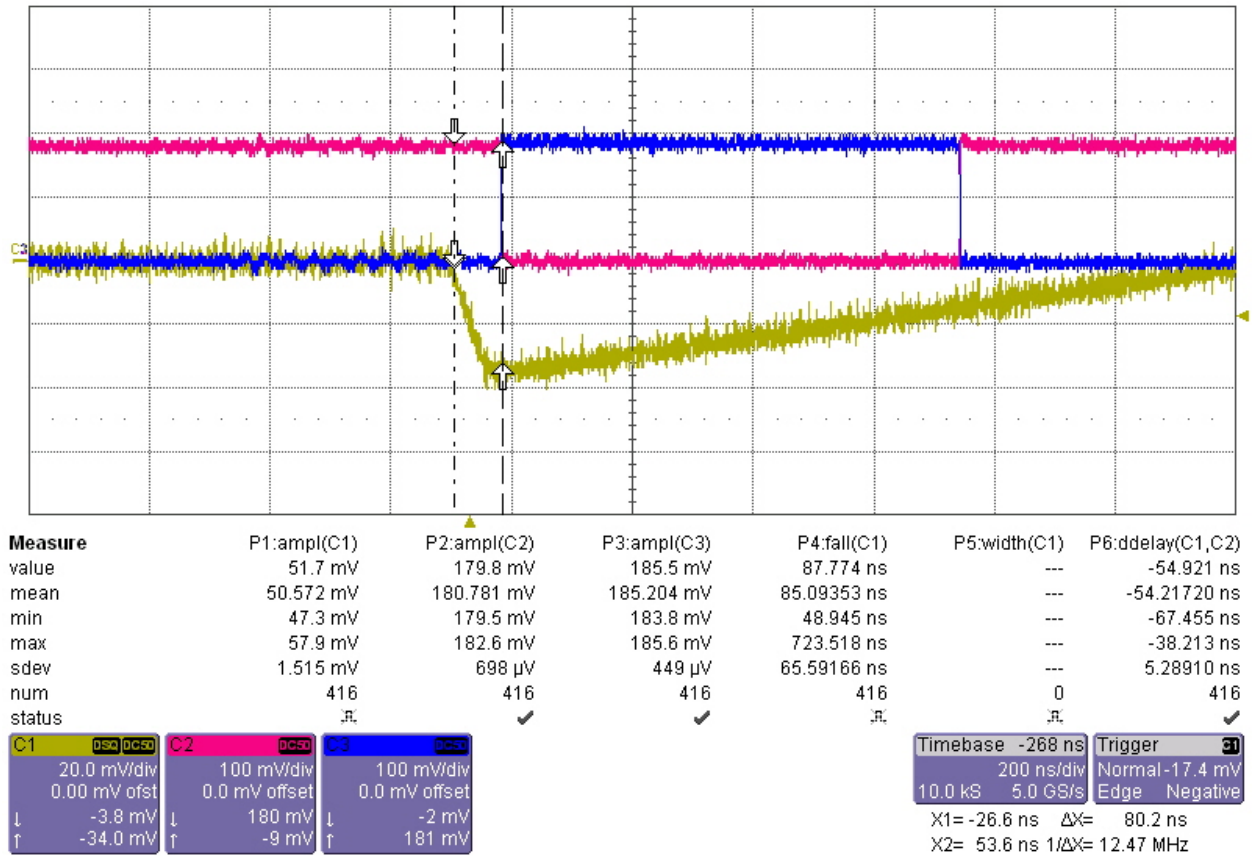
The output of channel no. 2 is connected to a front led that flashes when the input signal is above the threshold and it is useful for threshold regulation.

The module requires 2 power supply voltages: +5V and -5V. The derived currents are typically 310 mA (+5V) and 220 mA (-5V) without load . LVDS 100 Ohm loads add approximately 90 mA to the +5V supply current.

Test of the WDB

The fig. 3 shows the response of the module on negative pulses 35 mV amplitude with leading edge 50 ns and trailing edge 1000 ns. The module's threshold is set to -25 mV.

Fig. 3 Response of the WDB-16



The table 1 shows the WDB-16 characteristics.

Table 1. WDB-16 characteristics

| | |
|--|--|
| Number of channels | 16 |
| Limiting counting rate of each channel | $1 * 10^5 /s$ |
| Amplification factor | 25mV/uA |
| Dynamic range | 100 FC – 1000FC |
| Supply current, no load | Upos 5V, 307 mA; Uneg -5V, 216 mA |
| Output amplitude, 100 Ohm load | LVDS positive, 190 mV; LVDS negative, 190 mV |
| Delay | 80 ns |

Temperature output is $10 \text{ mV} / \text{C}^0$.

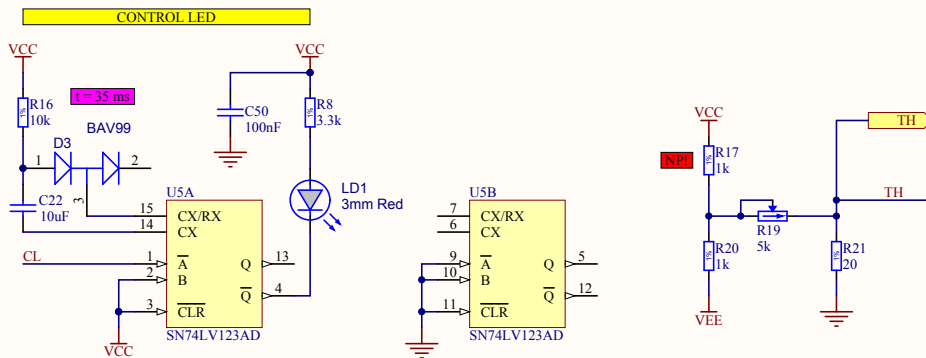
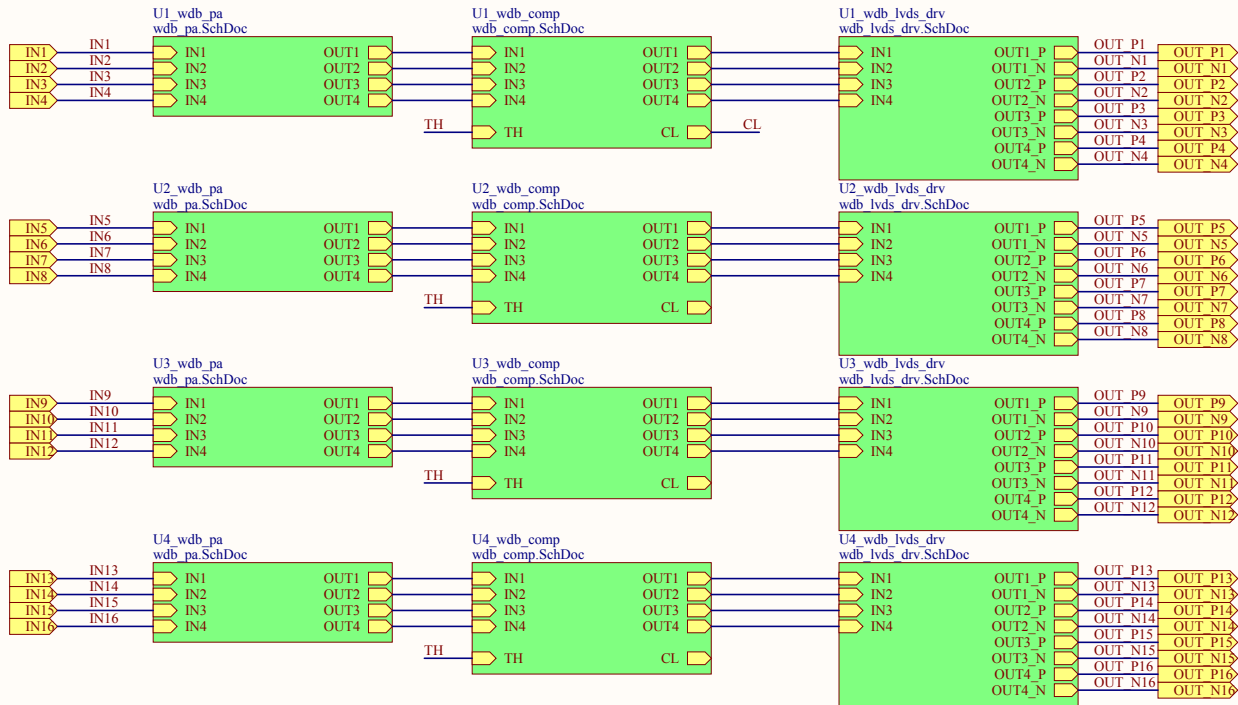
To turn the module's power off, connect the "OFF" contact to GND or supply to it 5V TTL/CMOS logic "0".

Table 2. Input connector IDC-34 pinout, front view

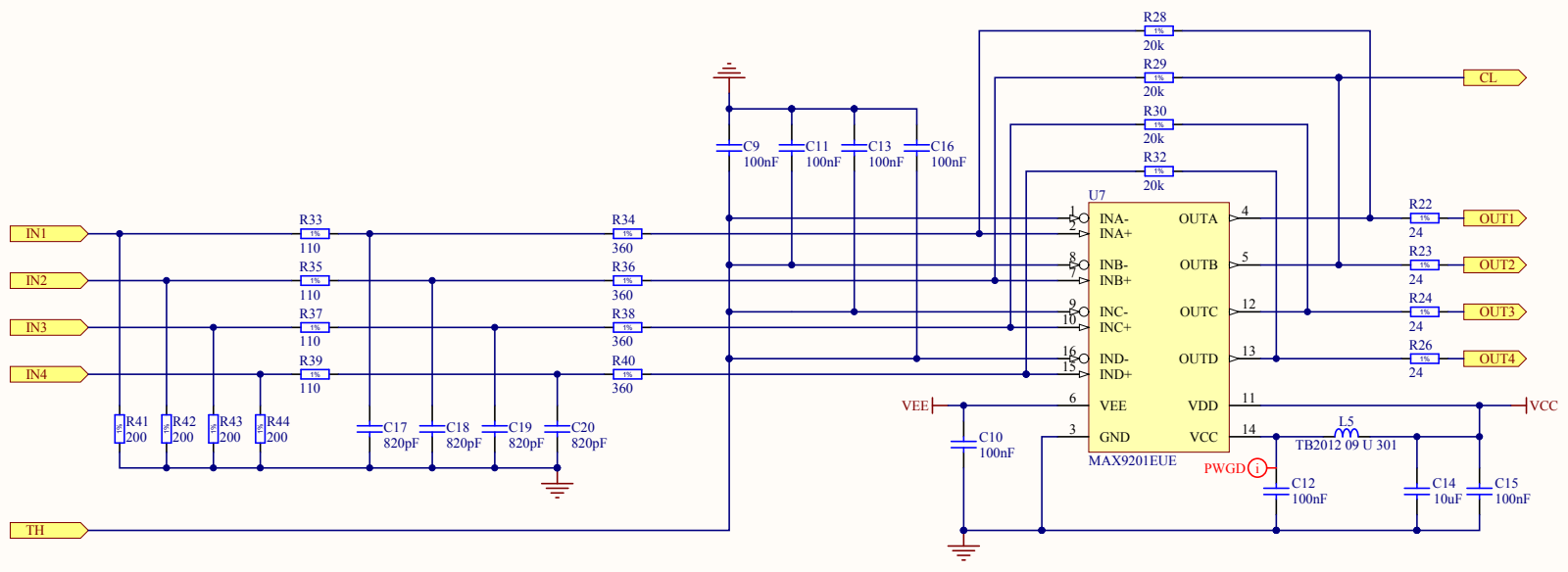
| Pin # | | Pin # | |
|-------|-------|-------|-------|
| 34 | FRAME | 33 | FRAME |
| 32 | GND | 31 | IN1 |
| 30 | GND | 29 | IN2 |
| 28 | GND | 27 | IN3 |
| 26 | GND | 25 | IN4 |
| 24 | GND | 23 | IN5 |
| 22 | GND | 21 | IN6 |
| 20 | GND | 19 | IN7 |
| 18 | GND | 17 | IN8 |
| 16 | GND | 15 | IN9 |
| 14 | GND | 13 | IN10 |
| 12 | GND | 11 | IN11 |
| 10 | GND | 9 | IN12 |
| 8 | GND | 7 | IN13 |
| 6 | GND | 5 | IN14 |
| 4 | GND | 3 | IN15 |
| 2 | GND | 1 | IN16 |

Table 3. Output connector pinout, rear view

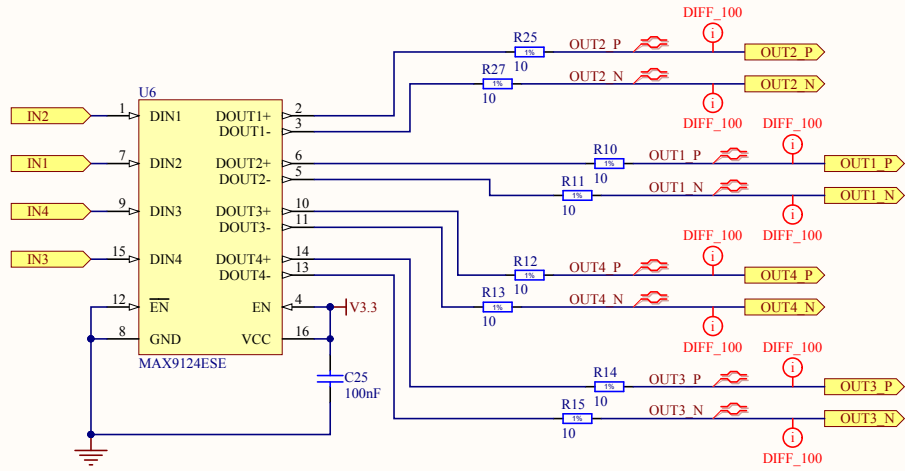
| Pin # | c | b | a |
|-------|--------|---|--------|
| 1 | OUT1- | | OUT1+ |
| 2 | OUT2- | | OUT2+ |
| 3 | OUT3- | | OUT3+ |
| 4 | OUT4- | | OUT4+ |
| 5 | OUT5- | | OUT5+ |
| 6 | OUT6- | | OUT6+ |
| 7 | OUT7- | | OUT7+ |
| 8 | OUT8- | | OUT8+ |
| 9 | OUT9- | | OUT9+ |
| 10 | OUT10- | | OUT10+ |
| 11 | OUT11- | | OUT11+ |
| 12 | OUT12- | | OUT12+ |
| 13 | OUT13- | | OUT13+ |
| 14 | OUT14- | | OUT14+ |
| 15 | OUT15- | | OUT15+ |
| 16 | OUT16- | | OUT16+ |
| 17 | | | |
| 18 | FRAME | | FRAME |
| 19 | FRAME | | FRAME |
| 20 | | | |
| 21 | T_GND | | TEMP |
| 22 | | | |
| 23 | | | |
| 24 | | | OFF |
| 25 | | | |
| 26 | 5V- | | 5V- |
| 27 | | | |
| 28 | GND | | GND |
| 29 | | | |
| 30 | 5V+ | | 5V+ |
| 31 | | | |
| 32 | GND | | GND |



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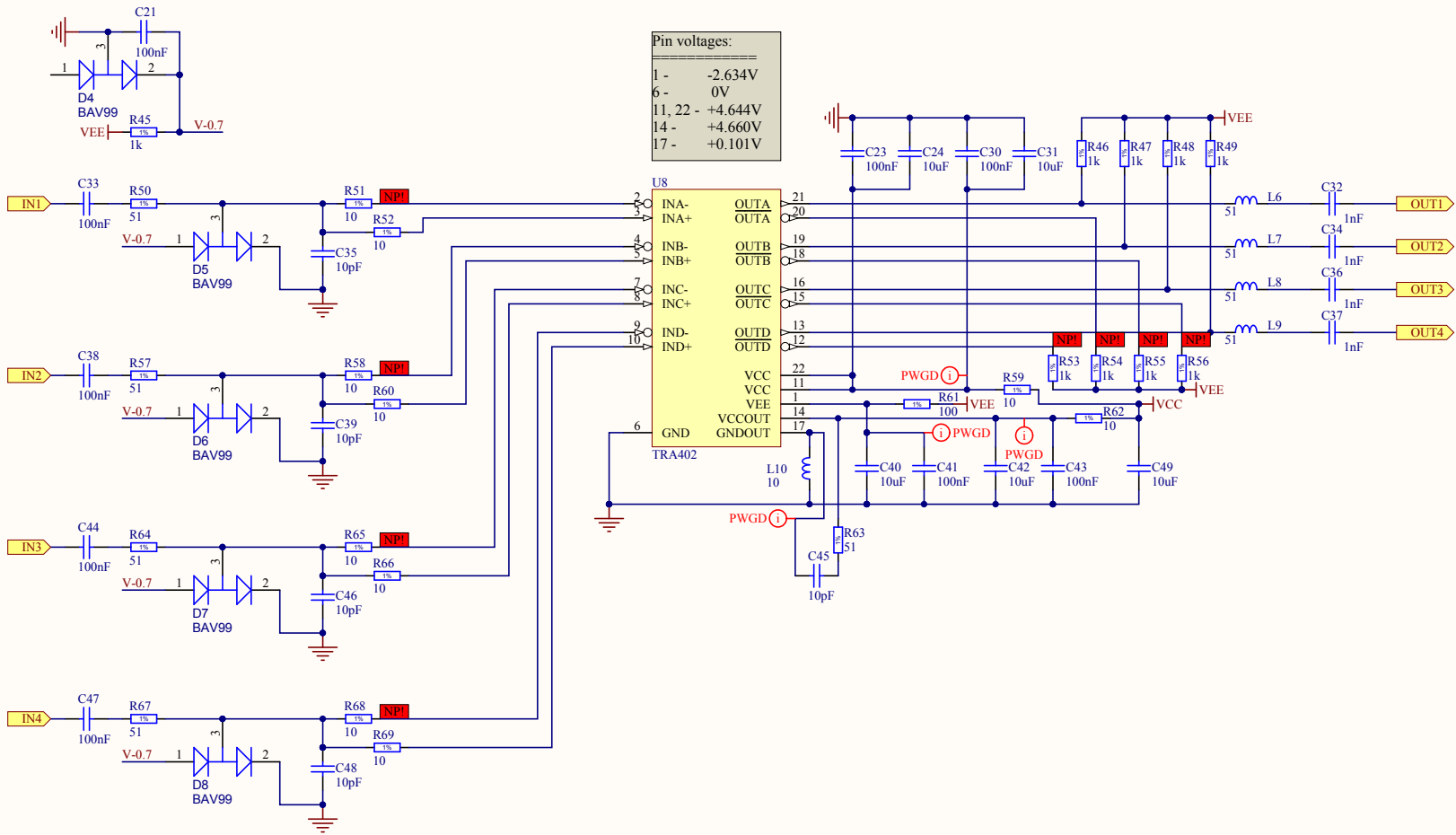
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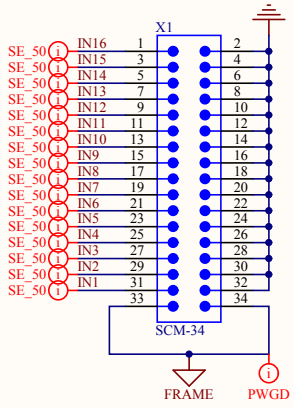
Pin voltages:

| | |
|----------|---------|
| 1 - | -2.634V |
| 6 - | 0V |
| 11, 22 - | +4.644V |
| 14 - | +4.660V |
| 17 - | +0.101V |

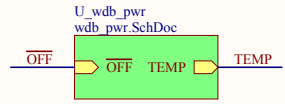
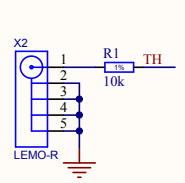


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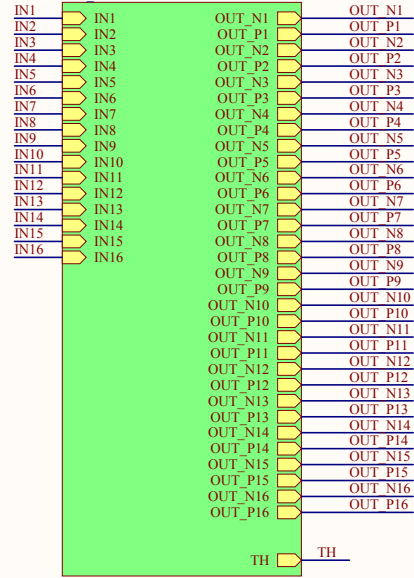
WIRE INPUT CONNECTOR



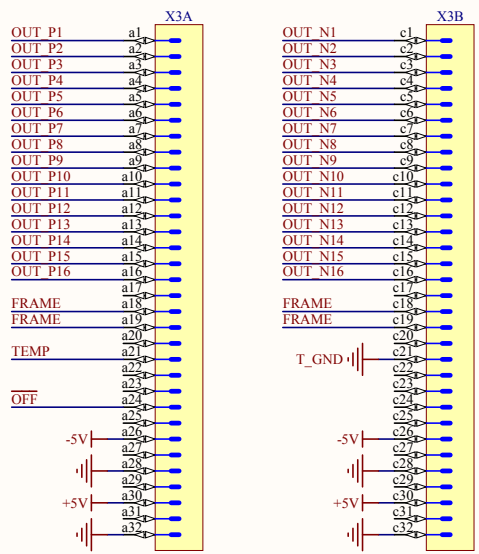
THRESHOLD CONTROL CONNECTOR



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wdb_ch.SchDoc

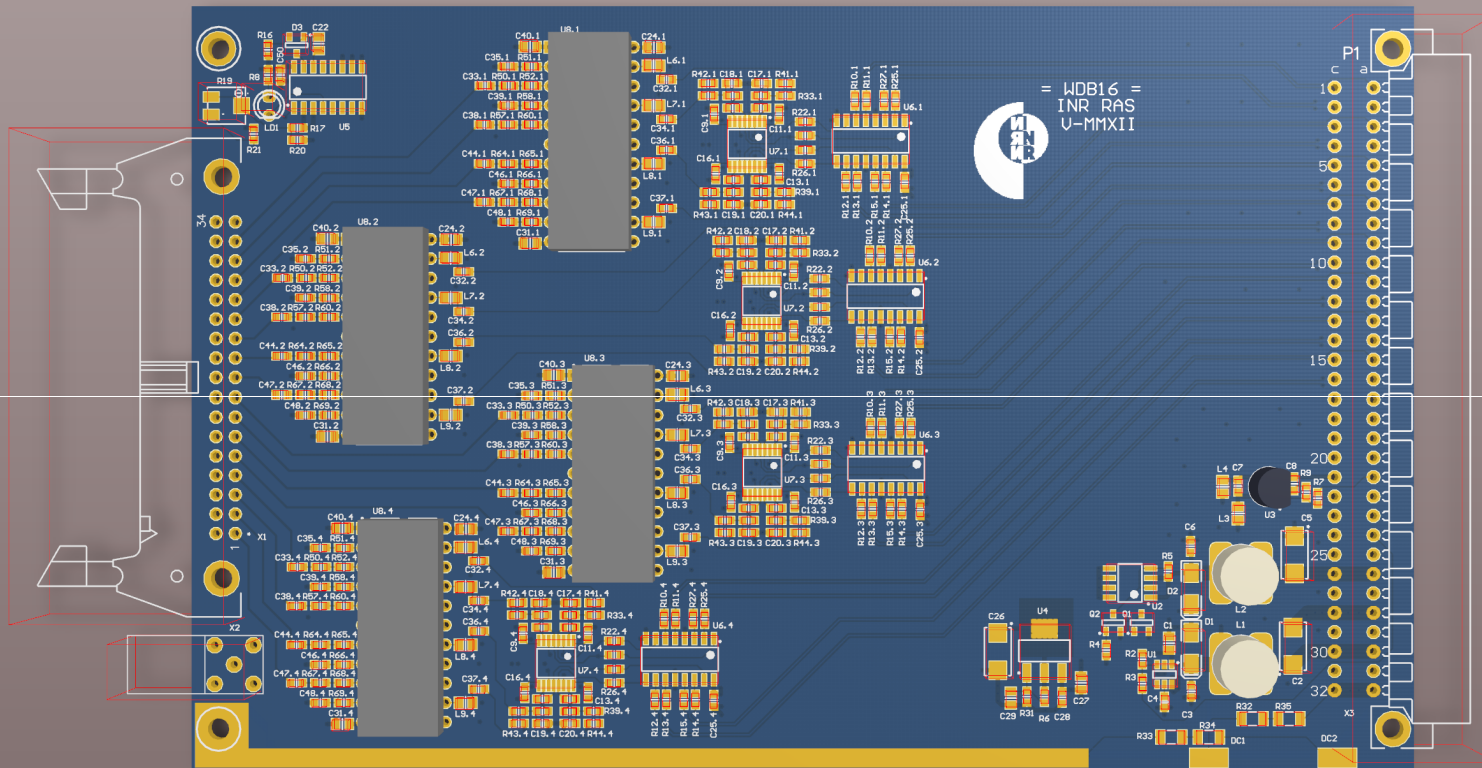


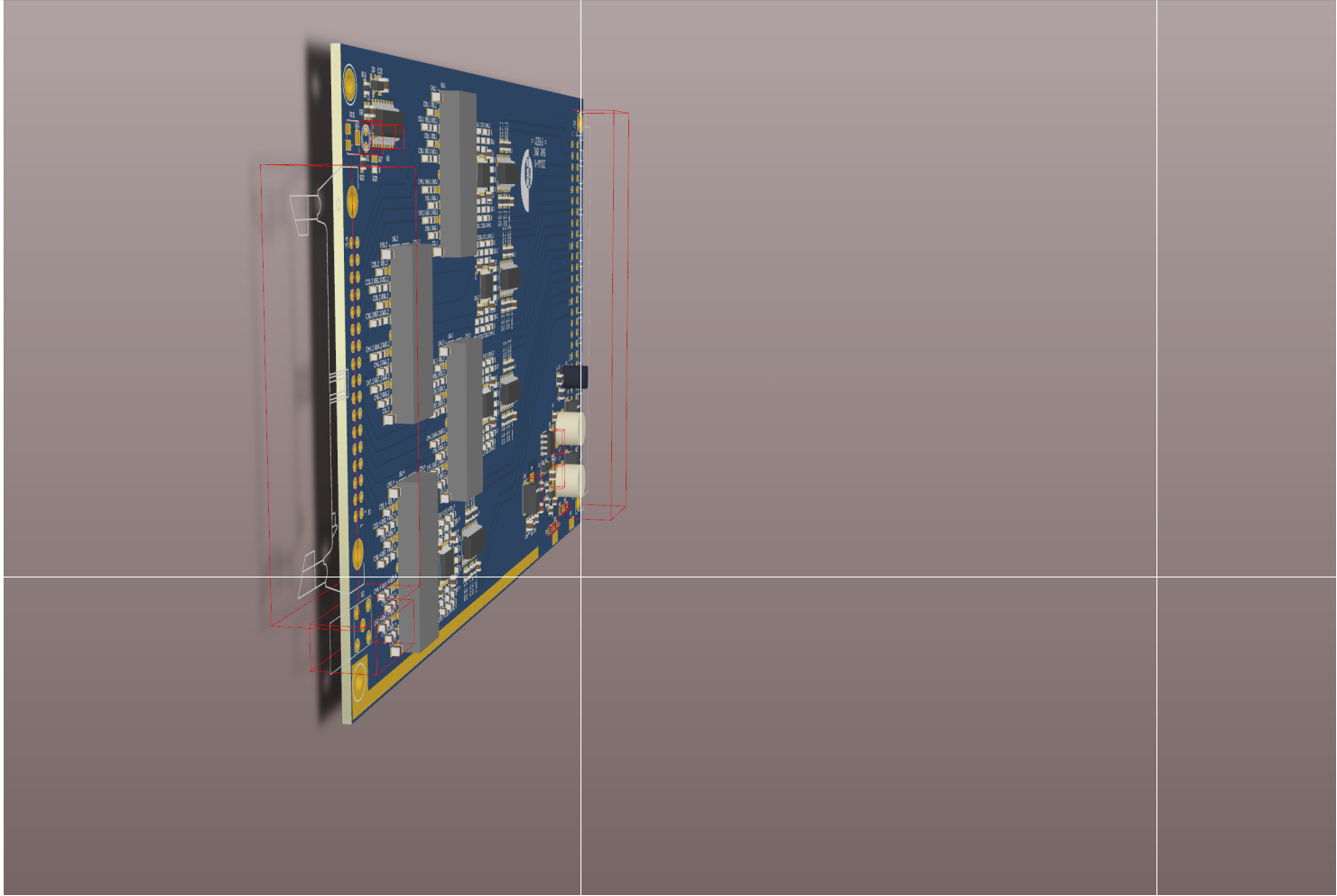
BACK PLANE CONNECTOR

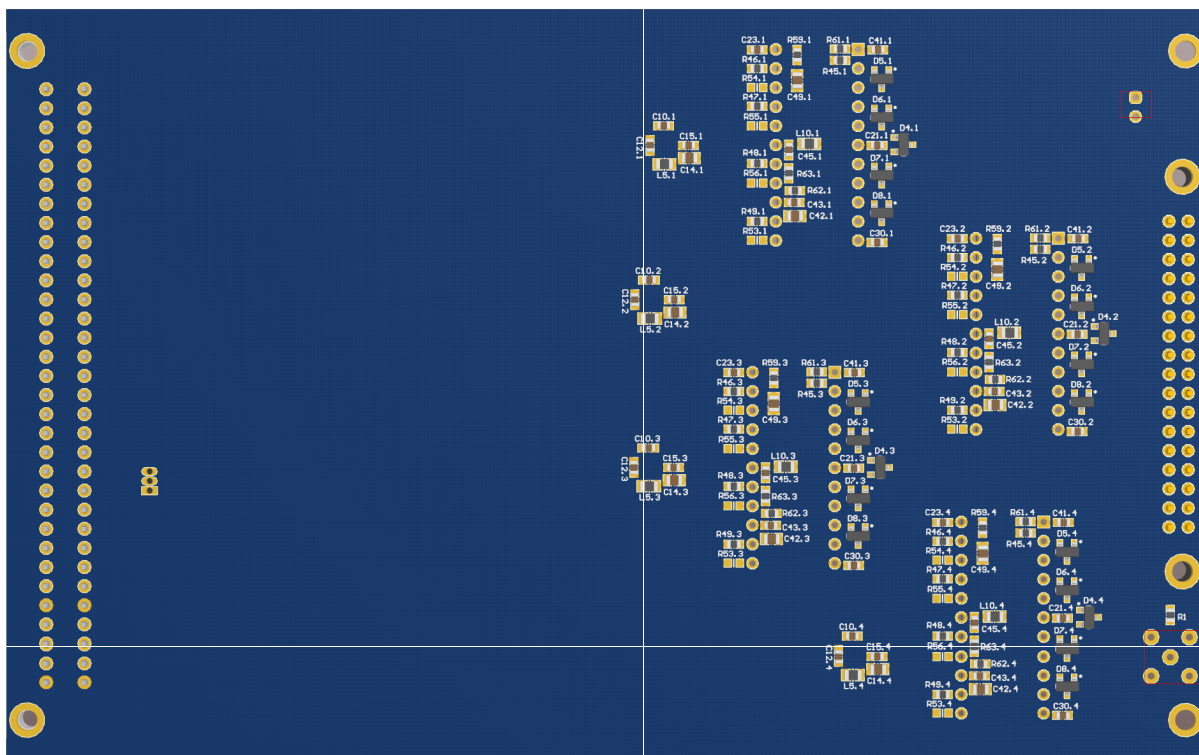


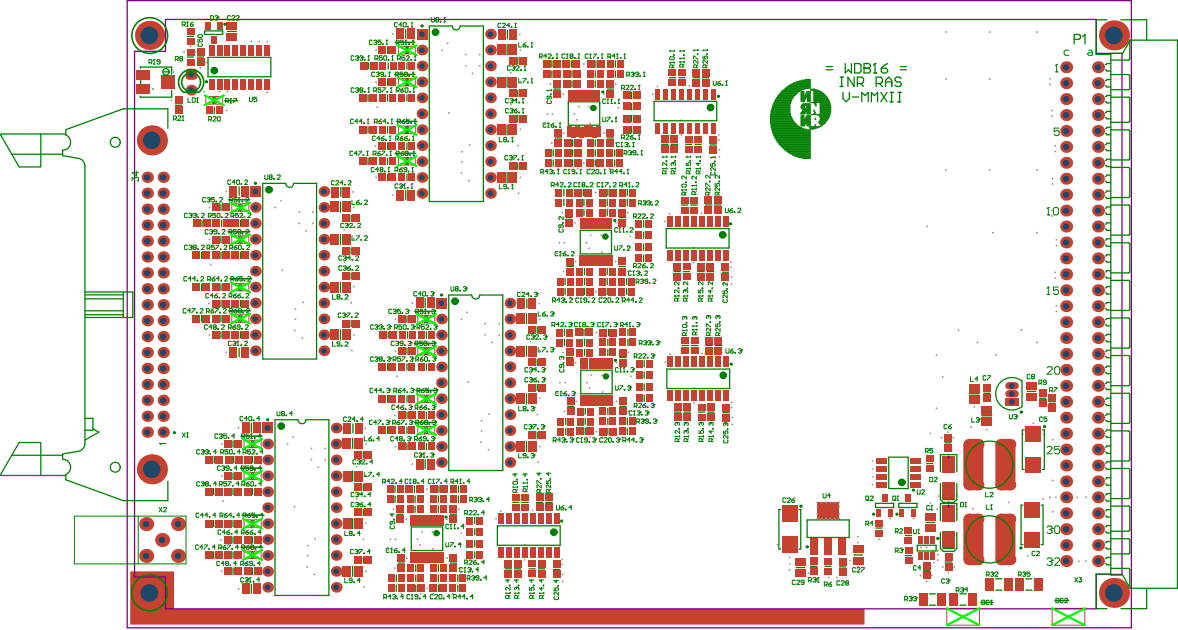
DIN41612, male 32x2 A,C right angle

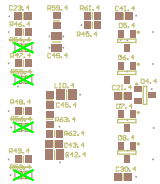
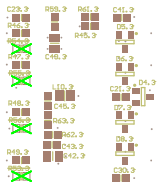
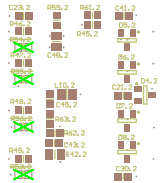
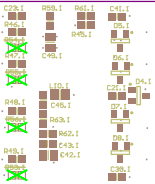
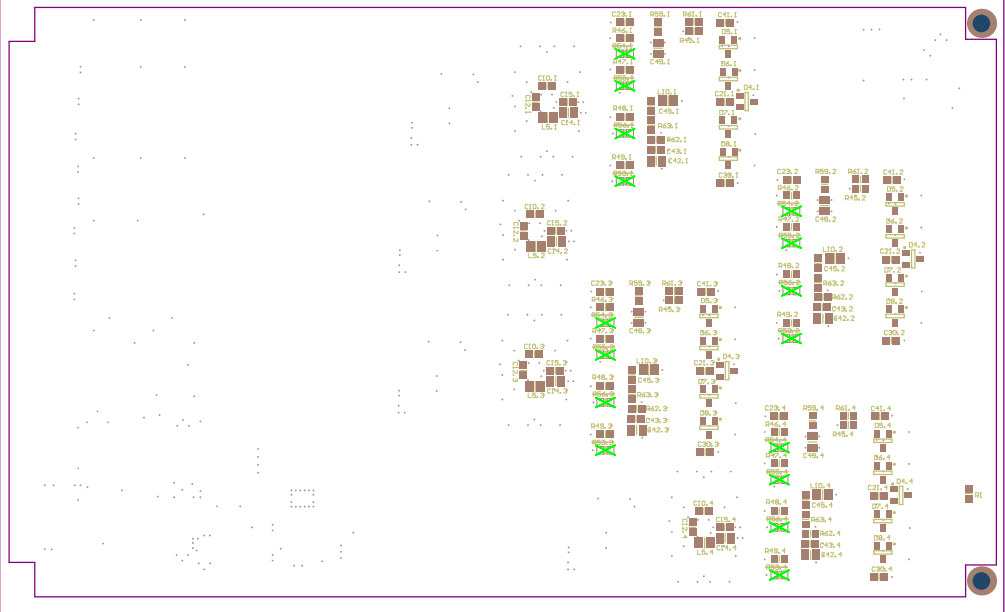
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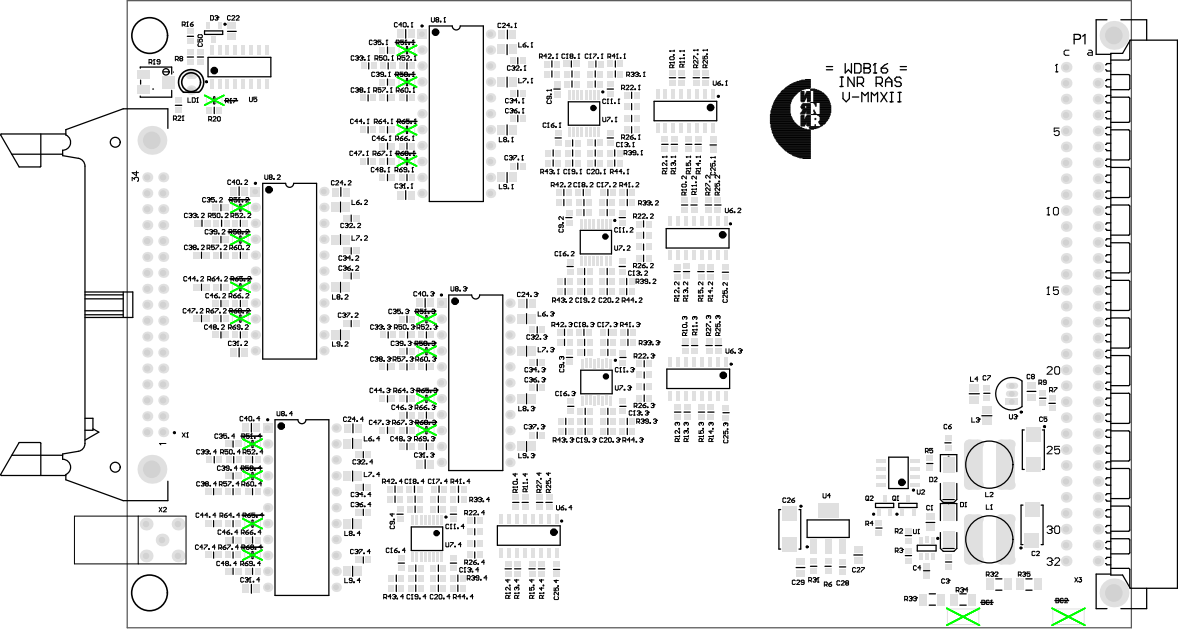


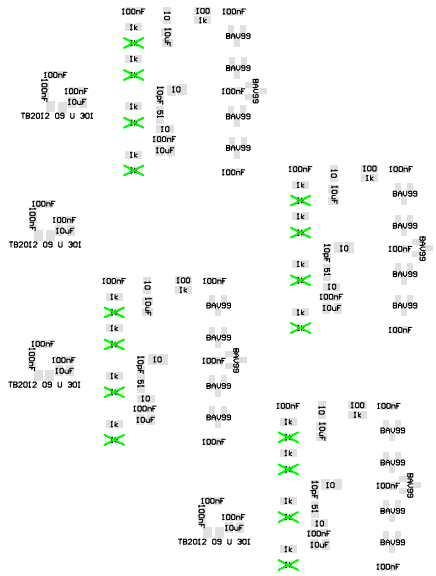






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10k